

## FEATURES

- Fully compliant with Ultra2 SCSI
- Provides Multimode Low Voltage Differential/Single-Ended (LVD/SE) termination for 9 signal line pairs
- Auto-selection of LVD or SE termination
- 5% tolerance on SE and LVD termination resistance
- Low power down capacitance of 3 pF
- Onboard thermal shutdown circuitry
- SCSI bus hot plug compatible
- Fully supports actively negated SE SCSI signals

## PIN ASSIGNMENT

VREF	1	36	TPWR
NC	2	35	HVD
NC	3	34	LVD
R1P	4	33	SE
R1N	5	32	R9N
R2P	6	31	R9P
R2N	7	30	R8N
HS GND	8	29	R8P
HS GND	9	28	HS GND
HS GND	10	27	HS GND
R3P	11	26	HS GND
R3N	12	25	R7N
R4P	13	24	R7P
R4N	14	23	R6N
R5P	15	22	R6P
R5N	16	21	DIFF_CAP
ISO	17	20	DIFFSENSE
GND	18	19	MSTR/SLV

DS2118MB 36-PIN SSOP

## DESCRIPTION

The DS2118M Ultra2 LVD/SE SCSI Terminator is both a Low Voltage Differential (LVD) and Single-Ended (SE) terminator. The multimode operation enables the designer to implement LVD in current products while allowing the end-user SE backward compatibility with legacy devices. If the device is connected in an LVD only bus, the DS2118M will use LVD termination. If any SE devices are connected to the bus, the DS2118M will use SE termination. This is accomplished automatically

inside the part by sensing the voltage on the SCSI bus DIFFSENS line.

For the LVD termination, the DS2118M integrates two regulated supplies with nine precision resistor strings. For the SE termination, one regulator and nine precision 110 ohm resistors are used. Three DS2118M terminators are needed for a Wide SCSI bus.

**REFERENCE DOCUMENTS**

SCSI Parallel Interface 2 (SPI-2) {X310/1142D}  
 SCSI-3 Parallel Interface (SPI) {X3T10/855D}  
 SCSI-3 Fast-20 {X3T10/1071D}  
 SCSI-2 {X3.131-1994}

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**FUNCTIONAL DESCRIPTION**

The DS2118M combines LVD and SE termination with DIFFSENS sourcing and detection. The LVD termination section consists of two source/sink amplifiers (VTOP, VBOT), biasing circuitry and nine precision resistor strings (RTOP, RMID, RBOT). The SE termination section consists of a 2.85V source/sink regulator with 9 precision 110 Ohm resistors. The DIFFSENSE section consists of a 1.3V, 5 mA driver and a sensing circuit (Figure 1).

The DS2118M's DIFF\_CAP pin monitors the DIFFSENS line to determine the proper operating mode of the device; this mode is indicated by the SE/LVD/HVD outputs. The DIFFSENSE pin can also drive the SCSI DIFFSENS line (when MSTR/SLV = 1) to determine the SCSI bus operating mode. The DS2118M switches to the termination mode that is appropriate for the bus based on the value of the DIFFSENS voltage. These modes are:

**LVD mode** LVD termination is provided by a precision laser trimmed resistor string with two amplifiers. This configuration yields a 105Ω differential and 150Ω common mode impedance. A fail-safe bias of 112 mV is maintained when no drivers are connected to the SCSI bus.

**SE mode** When the external driver for a given signal line turns off, the active terminator will pull that signal line to 2.85 volts (quiescent state). When used with an

active negation driver, the power amp can sink 22 mA per line while keeping the voltage reference in regulation. The terminating resistors maintain their 110Ω value.

**HVD Isolation Mode** The DS2118M identifies that there is an HVD (high voltage differential) device on the SCSI bus and isolates the termination pins from the bus.

When ISO = 1, the termination pins are isolated from the SCSI bus, and the bus mode indicators (SE/LVD/HVD) as well as Vref remains active. During thermal shutdown, the termination pins are isolated from the SCSI bus and Vref becomes high impedance. The diffsense driver is shut down during either of these two events.

To ensure proper operation, the TPWR pin should be connected to the SCSI bus TERMPWR line. As with all analog circuitry, the TERMPWR and VDD lines should be bypassed locally. A 2.2 μF capacitor and a 0.01 μF high frequency capacitor is recommended between TPWR and ground and placed as close as possible to the DS2118M. The DS2118M should be placed as close as possible to the SCSI connector to minimize signal and power trace length, thereby resulting in less input capacitance and reflections which can degrade the bus signals.

To maintain the specified regulation, a 4.7 μF capacitor is required between the Vref pin (VREF) and ground of each DS2118M. A high frequency cap (0.1 μF ceramic recommended) can also be placed on the Vref pin in applications that use fast rise/fall time drivers. A typical SCSI bus configuration is shown in Figure 2.

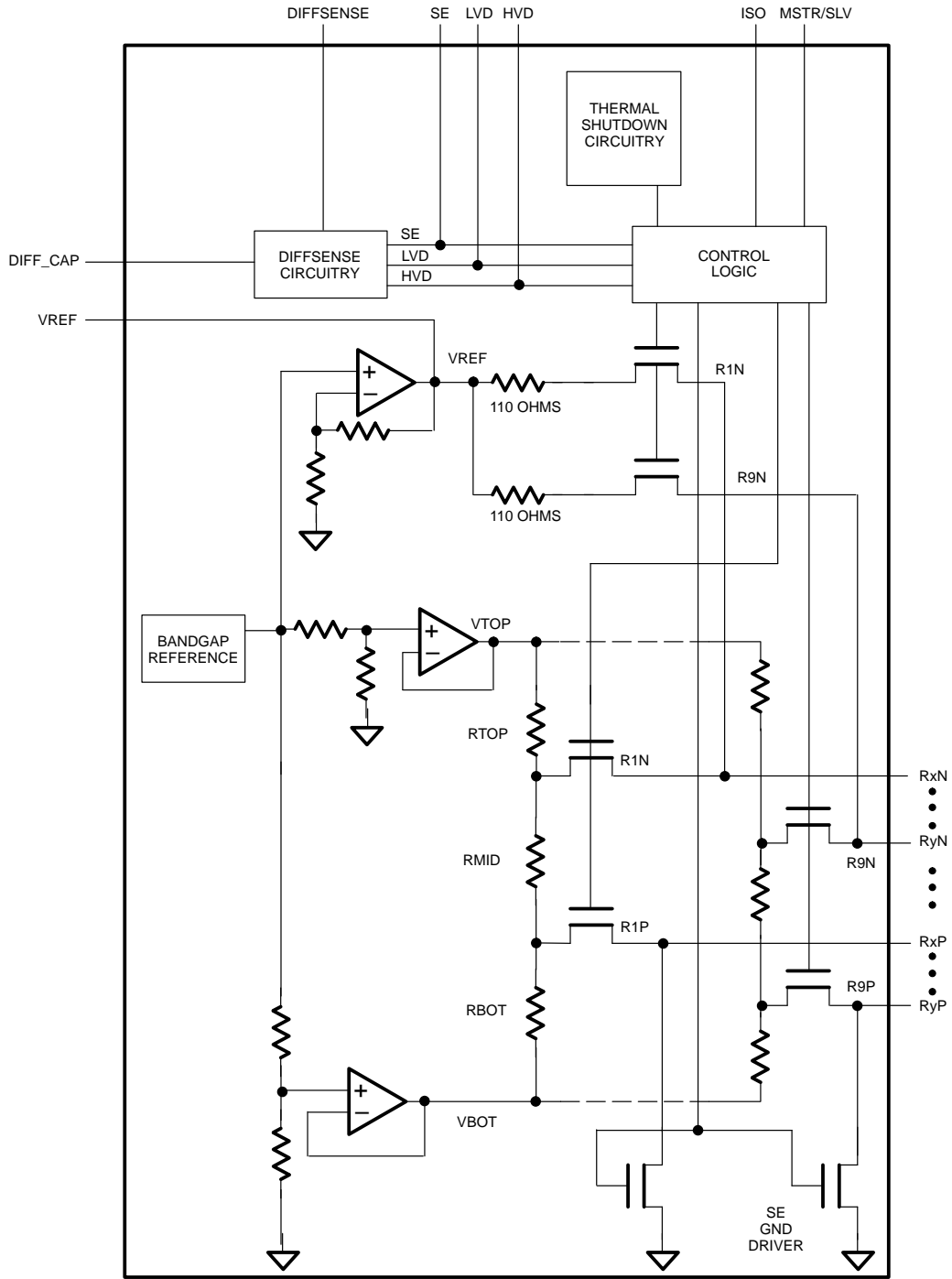
An internal pull-down resistor assures that the DS2118M will be terminating the bus if the ISO pin is left floating.

**NOTE:**

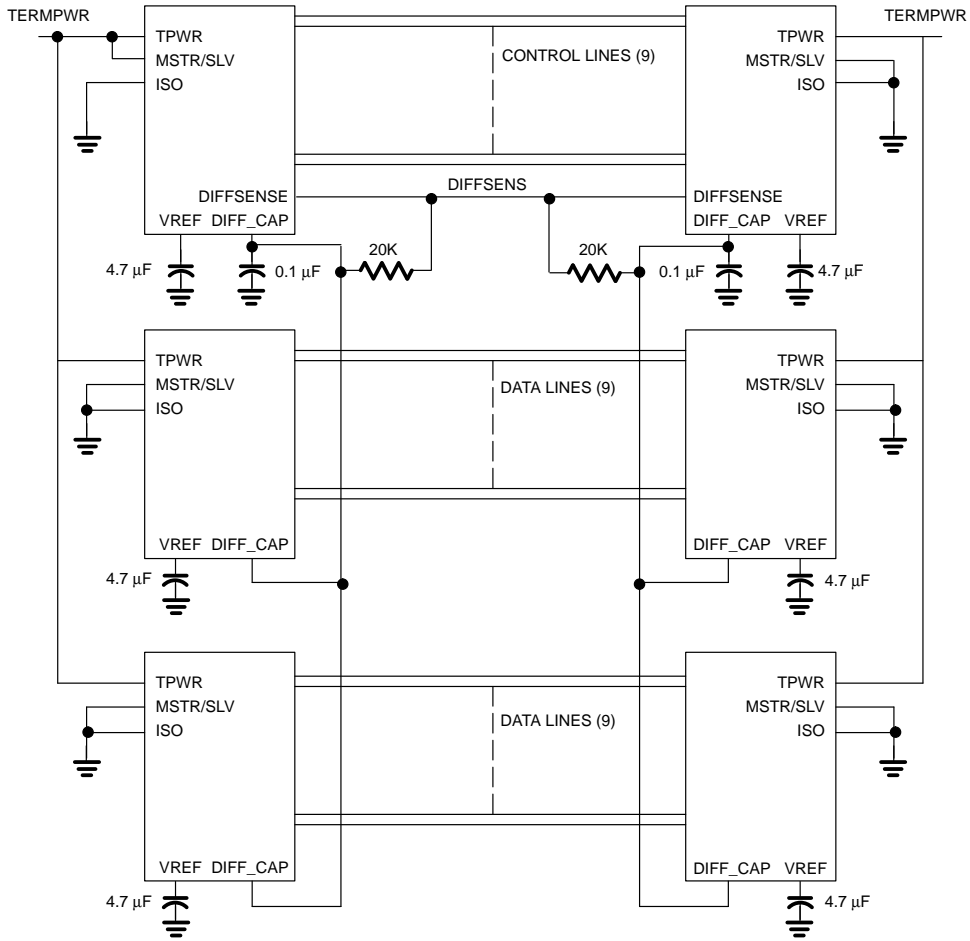
DIFFSENS – Refers to the SCSI bus signal.

DIFFSENSE – Refers to the Dallas Semiconductor pin name and internal circuitry relating to differential sensing.

DS2118M BLOCK DIAGRAM Figure 1



SCSI BUS CONFIGURATION Figure 2



**PIN DESCRIPTION** Table 1

PIN	SYMBOL	DESCRIPTION
1	VREF	<b>Reference Voltage.</b> 2.85 volt reference; must be decoupled with a 4.7 $\mu$ F cap.
2, 3	NC	<b>No Connect.</b> Do not connect these pins.
4–7, 11–16, 22–25, 29– 32	RxP RxN	<b>Signal Termination.</b> Connect to SCSI bus signal lines.
8, 10, 26, 9, 28, 27	HS GND	<b>Heat Sink Ground.</b> Internally connected to the mounting pad. Should be grounded.
17	ISO	<b>Isolation.</b> When pulled high, the DS2118M isolates it's bus pins (RxP, FxP) from the SCSI bus.
18	GND	<b>Ground.</b> Signal ground; 0.0 volts.
19	MSTR/SLV	<b>Master/slave.</b> Mode select for the non–controlling terminator. MSTR enables the DIFFSENSE driver.
20	DIFFSENSE	<b>DIFFSENSE.</b> Output to drive the SCSI bus DIFFSENS line.
21	DIFF_CAP	<b>DIFFSENSE CAPACITOR.</b> Connect 0.1 $\mu$ F capacitor for DIFFSENSE filter. Input to detect the type of device (differential or single–ended) on the SCSI bus.
33	SE	<b>Single–ended.</b> SE output of DIFFSENSE receiver; indicates SE bus operation.
34	LVD	<b>Low Voltage Differential.</b> LVD output of DIFFSENSE receiver; indicates LVD bus operation.
35	HVD	<b>High Voltage Differential.</b> HVD output of DIFFSENSE receiver; indicates HVD bus operation or thermal shutdown.
36	TPWR	<b>Terminator Power.</b> Connect to SCSI bus TERMPWR line and decouple with 2.2 $\mu$ F capacitor.

**RECOMMENDED OPERATING CONDITIONS**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Termpower Voltage	$V_{tpwr}$	4.0		5.50	V	
Logic 0	$V_{il}$	–0.3		+0.8	V	
Logic 1	$V_{ih}$	2.0		$V_{tpwr} + 0.3$	V	
Operating Temperature	$V_{amb}$	0		70	$^{\circ}$ C	

**SINGLE ENDED CHARACTERISTICS**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
SE Termination Resistance	Rse	104.5	110	115.5	Ohms	V <sub>line</sub> =0–3.0 volts
SE Voltage Reference	Vref	2.79		2.93	Volts	
SE Output Current	I <sub>ose</sub>			25.4	mA	V <sub>line</sub> = 0.2 volts
Output Capacitance	C <sub>out</sub>			3	pF	guaranteed by design

**LOW VOLTAGE DIFFERENTIAL CHARACTERISTICS**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Differential Mode Termination Resistance	R <sub>dm</sub>	100		110	Ohms	
Common Mode Termination Resistance	R <sub>cm</sub>	110		190	Ohms	
Differential Mode Bias	V <sub>dm</sub>	100		125	mV	All lines open
Common Mode Bias	V <sub>cm</sub>	1.125		1.375	V	

**DC CHARACTERISTICS**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Termpower Current	I <sub>tpmr</sub>		35		mA	All lines open
Input Leakage High	I <sub>ih</sub>	-1.0			uA	
Input Leakage Low	I <sub>il</sub>			1.0	uA	
Output Current High	I <sub>oh</sub>	-1.0			mA	V <sub>out</sub> = 2.4 volts; SE/LVD/HVD pins only
Output Current Low	I <sub>ol</sub>	4.0			mA	V <sub>out</sub> = 0.4 volts; SE/LVD/HVD pins only
DIFFSENS SE Operating Range	V <sub>seor</sub>	-0.3		0.5	V	
DIFFSENS LVD Operating Range	V <sub>lvdor</sub>	0.7		1.9	V	
DIFFSENS HVD Operating Range	V <sub>hvdor</sub>	2.4		V <sub>tpwr</sub> + 0.3	V	

**DC CHARACTERISTICS** cont'd

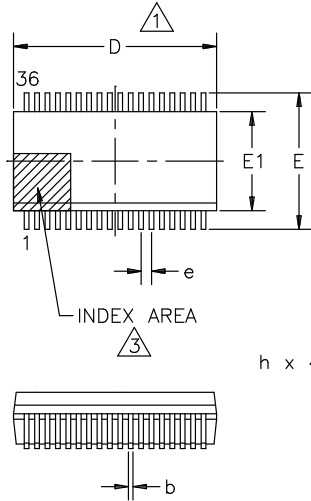
PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
DiffSense Driver Output Voltage	$V_{dso}$	1.2		1.4	V	MSTR/ SLV=1; $I_{ds}=0-5$ mA
DiffSense Driver Source Current	$I_{dsh}$		5	15	mA	MSTR/ SLV=1; $V_{dso}=0V$
DiffSense Driver Sink Current	$I_{dsl}$	20		200	uA	MSTR/ SLV=1; $V_{dso}=$ 2.75 V

**REGULATOR CHARACTERISTICS**

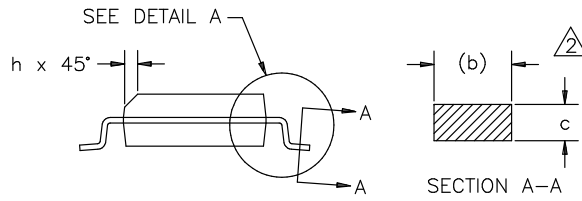
(0°C to 70°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	NOTES
Line Regulation	$L_{REG}$		1.0	2.0	%	
Load Regulation	$L_{OREG}$		1.3	3.0	%	
Current Limit	$I_{LIM}$		350		mA	
Sink Current	$I_{SINK}$	200			mA	

**DS2118M 36-PIN SSOP PACKAGE**



- △1 DIMENSIONS D AND E1 INCLUDE MOLD MISMATCH, BUT DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.254 MM PER SIDE.
- △2 SECTION A-A DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.13 TO 0.25 MM FROM THE LEAD TIP.
- △3 THE CHAMFER ON THE BODY IS OPTIONAL. IF IT IS NOT PRESENT, A VISUAL INDEX FEATURE MUST BE LOCATED WITHIN THE CROSS-HATCHED AREA.



DIM	MIN	MAX
A	2.44	2.64
A1	0.12	—
b	0.29	0.43
c	0.23	0.32
D	15.20	15.54
E	10.11	10.52
E1	7.40	7.60
e	0.80	BSC
h	0.25	0.71
L	0.51	1.02

DIMENSIONS ARE IN MILLIMETERS

