

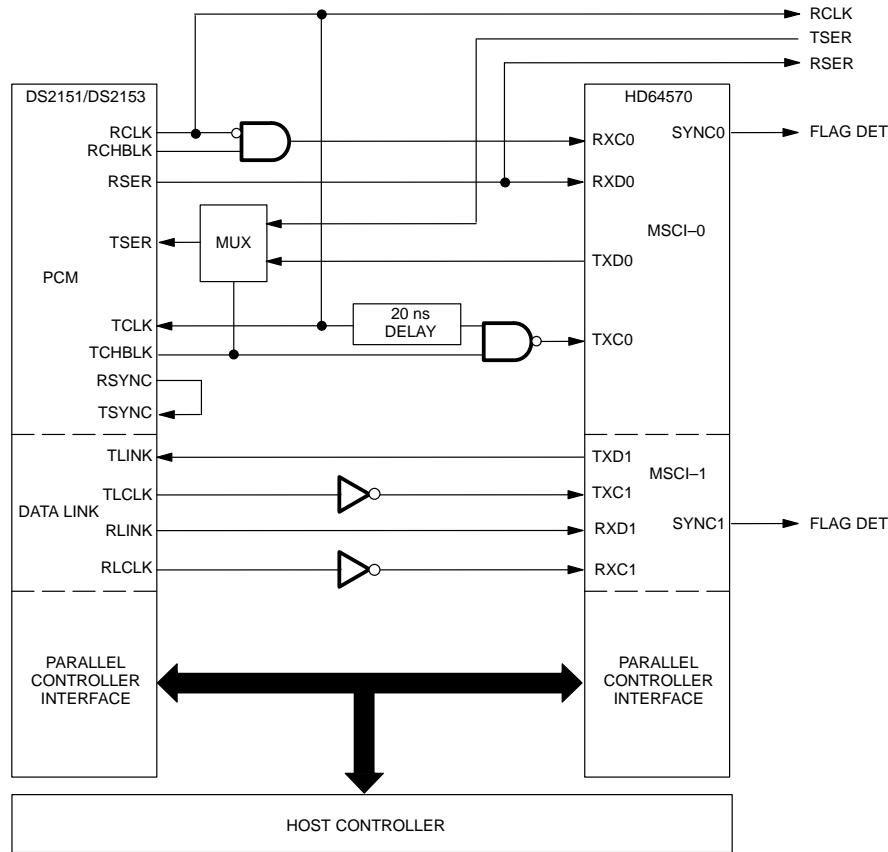


# Application Note 311

## DS2151, DS2153 PCM Interface, Interfacing to the Hitachi HD64570

PCM interface interconnections between the DS2151 and DS2153 and the hitachi HD64570 as shown in Figure 1.

**TRANSCEIVER – HD64570 SCA INTERCONNECTIONS** Figure 1



MCSI-0 and MCS-1 are in Bit Synchronous HDLC controller mode. MSCI-0 is used for B-Channel signaling while MCSI-1 is used for the FDL in T1 applications and Sa bits in E1 applications.

RSYNC. The Elastic Stores are disabled. SYSCCLK (not shown) should be connected to  $V_{SS}$ . RCHBLK and TCHBLK are used to independently select portions (DSO's) of the transmit and receive PCM streams.

The DS2151, DS2153 is shown in a "loop timed" configuration. TCLK and TSYNC are derived from RCLK and