

Application Note 332 DS2172 Bit Error Rate Tester, Simplified Receiver Operation

DS2172 SIMPLIFIED RECEIVER OPERATION DURING REPETITIVE PATTERN MODE PATTERN RECEIVE REGISTERS RL (PCR.3) _ 1 RDATA S1 _2 SHIFT REGISTER TAP CONTROLLED BY PATTERN LENGTH REGISTER A SYNC SYNC & RESYNC COMPARATOR В RLOS BIT ERROR COUNTER FNA RCLK -BIT COUNTER ENA NOTES: 1. ALL RECEIVE CIRCUITRY DISABLED WHEN RDIS = 1 2. CLOCKS NOT SHOWN FOR CLARITY 6 OUT OF 64 BITS IN ERROR AT A & B AND PCR.2 - 0 OUT OF SYNC SYNC -= 0 S1 = 1 IN SYNC SYNC -= 1 S1 = 2 PCR.1 32 CONSECUTIVE MATCHES AT A & B

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