Application Note 354
DS2152, DS2154 Clock Map

DALLAS
SEMICONDUCTOR

Figure 1. Logically describes the clock map of the DS2152 and DS2154 Single Chip Transceivers. Although there is only one Jitter Attenuator, which can be placed in the receive or transmit path, two are shown for simplification and clarity.

CLOCK MAP Figure 1
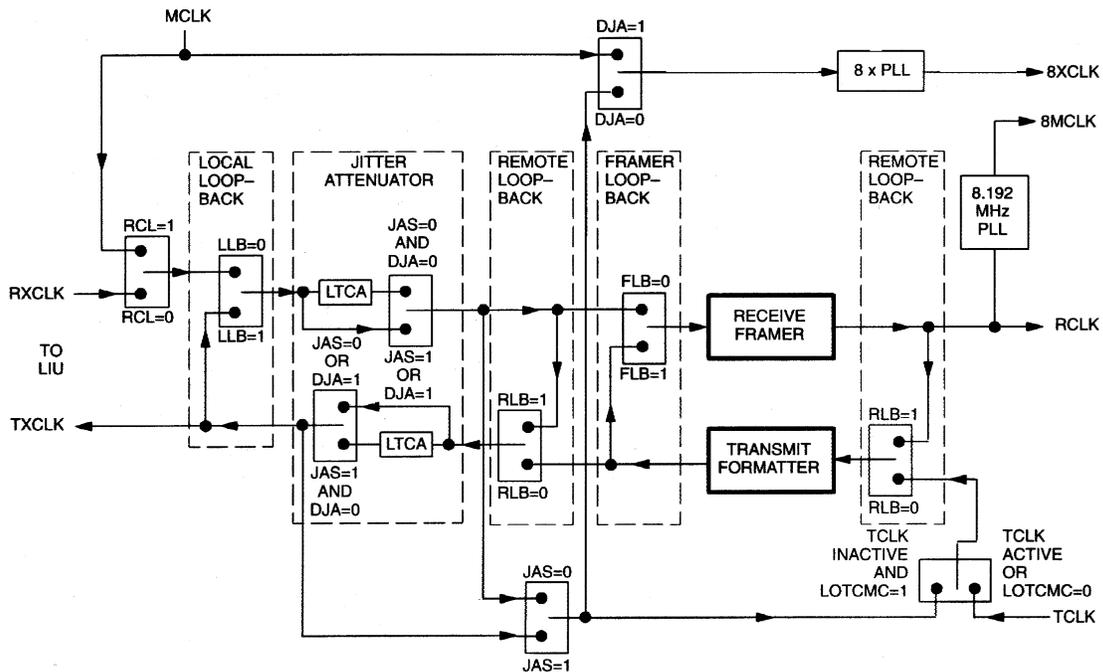
NOTES:

The PAYLOAD loopback switch is not included on the DS2154.

LTCA = Long Term Clock Average

RLB = Remote LoopBack	JAS = Jitter Attenuator Select
FLB = Framer LoopBack	DJA = Disable Jitter Attenuator
PLB = Payload LoopBack	RCL = Receive Carrier Loss
LLB=Local Loopback	LOTCMC = Loss Of Transmit Clock Mux Control

RXCLK = Recovered clock from LIU

TXCLK = Transmit rate clock for LIU