DESCRIPTION

Demonstration circuit 2158A hosts a high accuracy RMS Power Detector featuring the LTC®5596 IC. This device is a wide dynamic range RMS RF Power Detector operational from 100MHz to 40GHz. The input dynamic range with 1dB accuracy is up to 35dB depending on frequency. The detector output voltage slope is normally 29mV/dB. The DC2158A Demo Circuit is optimized for wide operational frequency range up to 40GHz with 2.9mm SMK edge mount connector. Input impedance to LTC5596 is internally matched to 50Ω. It is suitable for RMS measurements of high crest factor waveforms up to 12dB peak/average ratio. No external coupling capacitor is necessary if DC voltage at RFIN pin is kept below 1.0V. CAUTION: This part is sensitive to electrostatic discharge (ESD). Observe proper ESD precautions when handling the LTC5596.

Design files for this circuit board are available at http://www.linear.com/demo/DC2158A

ABSOLUTE MAXIMUM INPUT RATINGS

(Note 1)
Supply Voltage(VCC): ................................................ 3.8V
DC Voltage at RFIN: ........................................ –0.3V to 1.0V
DC Voltage at FLTR: .................................. –0.3V to 0.4V
DC Voltage at EN: ...................................... –0.3V to 3.8V
RFIN Input Power-Average: .............................. 15dBm
TJMAX .......................................................... 150°C
Case Operating Temperature Range ........ –40°C to 105°C
Storage Temperature Range ........ –65°C to 150°C
Note 1: Voltage on all pins must not exceed VCC+0.3V or be less than –0.3V.

TEST SETUP

Figure 1: Test Setup for RF Performance Measurements
NOTES ON TEST EQUIPMENT AND SETUP

- Use a high performance signal generator with accurate output power levels up to 40GHz, such as Rohde & Schwarz SMF100A.
- Demo Board DC2158A includes the SMK 2.9mm connector for best performance up to 40GHz.
- Connecting cable for RF signal should be rated up to 40GHz for the best performance.
- Optional input attenuation can be used to improve return loss, but also shifts the log intercept point accordingly.
- Use high quality power supply with low noise, able to support 3.3V with 40mA of current.

QUICK START PROCEDURE

1. Remove the DC2158A from its protective packaging in an ESD-safe working area (see Figure 1).
2. With power supply turned off, connect VCC and EN turret to the supply positive output. And GND to the negative supply output. Keep the leads to be as short as possible to avoid voltage overshoot.
3. Slowly ramp up the VCC together with EN supply to 3.3V. Recommended ramp time is 1ms minimum. Do not float EN.
4. Connect the RF input to the signal generator at the 2.92mm connector.
5. Set the frequency and power level (less than +10dBm) of the signal generator.
6. Measure the output voltage with the DMM.
7. Sweep the power level to generate the transfer curve VOUT vs Input Power.
**DEMO BOARD USAGE NOTES**

1. Demo Board DC2158A has provisions for interstage filter cap. Additional capacitor (C3) can be installed to slow down the transient response to reduce the output ripple. Short R5 and install C3. The range for C3 is 10pF to 1nF.

2. The detector output slope is approximately 29mV/db.

3. Linear regression is used to generate the slope and intercept point from the best fit straight line.

4. Linearity Error = \( \frac{V_{OUT}}{Slope} + \log \text{Intercept} - \text{Input Power} \).

5. A minimum two point calibration is necessary for most applications. Additional calibration points will improve the accuracy of the power detection.
## PARTS LIST

<table>
<thead>
<tr>
<th>ITEM</th>
<th>QTY</th>
<th>REFERENCE</th>
<th>PART DESCRIPTION</th>
<th>MANUFACTURER/PART NUMBER</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>C1</td>
<td>CAP, 100nF, FOR 40GHz, 0402</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>1</td>
<td>C3, C4, R5, R6, R7, R8, C10</td>
<td>NC, 0402</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>7</td>
<td>C5, C6, C7</td>
<td>NC, 0402</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>3</td>
<td>C8</td>
<td>CAP, NPO, 10pF, 5%, 50V, 0402</td>
<td>MURATA, GRM1555C1H100JA01D</td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td>C9</td>
<td>NC, 0402</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>4</td>
<td>E1-E4</td>
<td>TURRET, PAD .061&quot;</td>
<td>MILL-MAX, 2308-2-00-80-00-00-07-0</td>
</tr>
<tr>
<td>6</td>
<td>1</td>
<td>J1</td>
<td>CONN, 2.9mm JACK TO EDGE-LAUNCH, DC - 40GHz</td>
<td>SRI CON., 25-146-1000-93</td>
</tr>
<tr>
<td>8</td>
<td>3</td>
<td>J2-J4</td>
<td>CONN, SMA 50Ω EDGE-LAUNCH</td>
<td>E. F. JOHNSON, 142-0701-851</td>
</tr>
<tr>
<td>9</td>
<td>1</td>
<td>R1</td>
<td>RES, CHIP, 470, 1%, 0402</td>
<td>VISHAY, CRCW0402470RFKED</td>
</tr>
<tr>
<td>10</td>
<td>1</td>
<td>R2</td>
<td>RES, CHIP, 1%, 0402</td>
<td>VISHAY, CRCW04021R00FNED</td>
</tr>
<tr>
<td>11</td>
<td>1</td>
<td>R4</td>
<td>NC, 0402</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>1</td>
<td>U1</td>
<td>IC, LINEAR TECHNOLOGY, LTC5596IDC, DFN 2 × 2</td>
<td>LINEAR TECH., LTC5596IDC</td>
</tr>
</tbody>
</table>
Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.
DEMO MANUAL DC2158A

DEMONSTRATION BOARD IMPORTANT NOTICE

Linear Technology Corporation (LTC) provides the enclosed product(s) under the following AS IS conditions:

This demonstration board (DEMO BOARD) kit being sold or provided by Linear Technology is intended for use for ENGINEERING DEVELOPMENT OR EVALUATION PURPOSES ONLY and is not provided by LTC for commercial use. As such, the DEMO BOARD herein may not be complete in terms of required design-, marketing-, and/or manufacturing-related protective considerations, including but not limited to product safety measures typically found in finished commercial goods. As a prototype, this product does not fall within the scope of the European Union directive on electromagnetic compatibility and therefore may or may not meet the technical requirements of the directive, or other regulations.

If this evaluation kit does not meet the specifications recited in the DEMO BOARD manual the kit may be returned within 30 days from the date of delivery for a full refund. THE FOREGOING WARRANTY IS THE EXCLUSIVE WARRANTY MADE BY THE SELLER TO BUYER AND IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED, IMPLIED, OR STATUTORY, INCLUDING ANY WARRANTY OF MERCHANTABILITY OR FITNESS FOR ANY PARTICULAR PURPOSE. EXCEPT TO THE EXTENT OF THIS INDEMNITY, NEITHER PARTY SHALL BE LIABLE TO THE OTHER FOR ANY INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES.

The user assumes all responsibility and liability for proper and safe handling of the goods. Further, the user releases LTC from all claims arising from the handling or use of the goods. Due to the open construction of the product, it is the user's responsibility to take any and all appropriate precautions with regard to electrostatic discharge. Also be aware that the products herein may not be regulatory compliant or agency certified (FCC, UL, CE, etc.).

No License is granted under any patent right or other intellectual property whatsoever. LTC assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or any other intellectual property rights of any kind.

LTC currently services a variety of customers for products around the world, and therefore this transaction is not exclusive. Please read the DEMO BOARD manual prior to handling the product. Persons handling this product must have electronics training and observe good laboratory practice standards. Common sense is encouraged.

This notice contains important safety information about temperatures and voltages. For further safety concerns, please contact a LTC application engineer.

Mailing Address:

Linear Technology
1630 McCarthy Blvd.
Milpitas, CA 95035

Copyright © 2004, Linear Technology Corporation